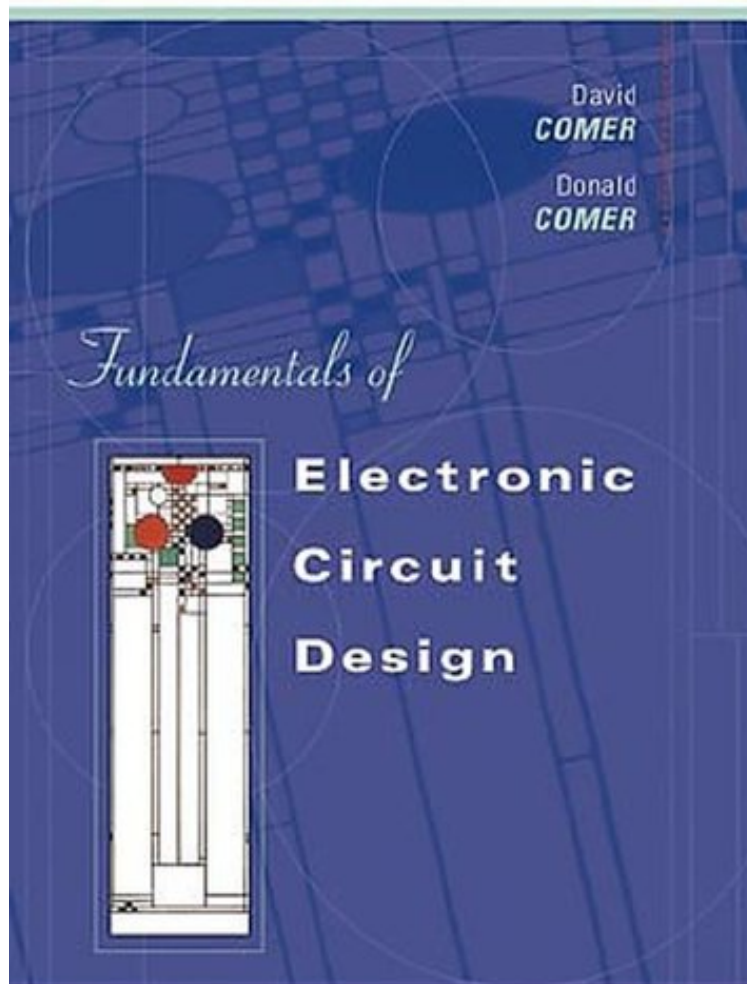


Fundamentals of Electronic Circuit Design

David J. Comer, Donald T. Comer
DOC | *audiobook | ebooks | Download PDF | ePub



[Download](#)

[Read Online](#)

#1856262 in Books 2002-05-21 Original language: English PDF # 1 10.10 x 1.00 x 7.80l, 2.47 #File Name: 0471410160512 pages | File size: 39.Mb

David J. Comer, Donald T. Comer : Fundamentals of Electronic Circuit Design before purchasing it in order to gauge whether or not it would be worth my time, and all praised Fundamentals of Electronic Circuit Design:

1 of 1 people found the following review helpful. Not Worthy of Being on Your Shelf By APK This textbook will not assist you in learning electronics. I have identified multiple reasons for why I think it cannot which include excessive equations, lack of explanations, and poor assumptions. The number of equations given throughout the textbook is excessive. The author had the need to derive countless special cases instead of providing generic equations applicable to all circuits. The lack of explanations problem is one that seems to plague electronics textbooks and includes this one. Equations are dumped into the text followed by some brief description of the variables or constants involved, rather than building up to an equation. A good example is on page 135 where a lengthy equation is immediately followed by: "...where k is Boltzmann's constant, T is the absolute temperature, and q is the electronic charge." This pattern is not

uncommon throughout the text. Another point I must mention is the fact that no further explanation of these variables or constants are given. This leads to my next point. The authors assume too much about the readers of this textbook. Equations are introduced which, somehow, are derived from other equations that may or may not have been discussed. Kirchoff's Laws are not in the scope of this book, but the author will use them without citation. Failure to define how this new equation was derived leaves the reader guessing. Another example on topic is when new terms are used and not defined. A perfect example is when discussing bipolar junction transistors (BJT) the term "active region" is used. I have never seen nor read the definition in the text. 0 of 0 people found the following review helpful. More difficult than it should be

By TailsFox88 This book is far more confusing than it needs to be. It was clearly written for experts in circuit design and not for students. Frequently, the book will skip several steps under the assumption that you understand the reasoning and steps in between. However, if it's your first time learning about transistors, you end up more confused when the chapter is finished. Problems in the back are also equally frustrating, and often times appear to be completely contradictory to what is "explained" in the chapter. Do yourself a favor, if you are taking a class that requires this book, take it from someone else. This book is evil. 0 of 0 people found the following review helpful.

Terrible textbook, waste of money

By Colby D. Renfro This book is absolutely unintelligible in the places that can be the most confusing. It is not structured in the slightest, the equations are inconsistent, and the actual problems require you to use techniques not shown in the book at all. Random assumptions are pulled out of thin air and the diagrams have subscripts that aren't referenced anywhere else in the chapter. I have no idea why the professor uses this. His lectures are painfully trivial, and the book is incomprehensible, which makes for a very painful experience.

Three chapters emphasize IC design, with SPICE simulations integrated into each one. * Concise, streamlined presentation of topics.